

<b>STUDY MODULE DESCRIPTION FORM</b>		
Name of the module/subject <b>Modulation methods in power electronics</b>		Code <b>1010311371010326897</b>
Field of study <b>Electrical Engineering</b>	Profile of study (general academic, practical) <b>(brak)</b>	Year /Semester <b>4 / 7</b>
Elective path/specialty <b>Microprocessor's Control Systems in</b>	Subject offered in: <b>Polish</b>	Course (compulsory, elective) <b>obligatory</b>
Cycle of study: <b>First-cycle studies</b>	Form of study (full-time, part-time) <b>full-time</b>	
No. of hours Lecture: <b>15</b> Classes: <b>-</b> Laboratory: <b>15</b> Project/seminars: <b>-</b>		No. of credits <b>3</b>
Status of the course in the study program (Basic, major, other) <b>(brak)</b>		(university-wide, from another field) <b>(brak)</b>
Education areas and fields of science and art <b>technical sciences</b> <b>Technical sciences</b>		ECTS distribution (number and %) <b>3 100%</b> <b>3 100%</b>
<b>Responsible for subject / lecturer:</b>  dr hab. inż. Ryszard Porada, prof. nadzw. email: ryszard.porada@put.poznan.pl tel. 48 61 665 2360 Wydział Elektryczny ul. Piotrowo 3A 60-965 Poznań		
<b>Prerequisites in terms of knowledge, skills and social competencies:</b>		
1	<b>Knowledge</b>	It has rudimentary knowledge from the range of electrical engineering and the signals theory.
2	<b>Skills</b>	It knows to use rudimentary knowledge from the range of the electrical engineering and the signals theory.
3	<b>Social competencies</b>	It can think and work enterprisingly in the area of the designing of systems with modulation.
<b>Assumptions and objectives of the course:</b> Learning of analog and digital modulation methods and their use in the power electronics.		
<b>Study outcomes and reference to the educational results for a field of study</b>		
<b>Knowledge:</b>		
1. to describe rules of the modulation and to apply tools of the spectral analysis on basic level - [K_W04+ K_W22+++]		
<b>Skills:</b>		
1. to apply a principle the modulation and to use tools of the spectral analysis on basic level - [K_U03 ++ K_U17 ++]		
<b>Social competencies:</b>		
1. it can think and work enterprisingly in the area of the designing of power electronics systems in the aspect of the modulation of exit quantity - [K_K02 ++]		
<b>Assessment methods of study outcomes</b>		

<p>Lecture</p> <p>? the credit of the lecture preceded with the credit of occupations laboratory exercises and project,</p> <p>Designing work and laboratory exercises:</p> <p>? the test and awarding the knowledge of need-to-know to realization of placed problems in the given area of tasks,</p> <p>? verification skills on every exercises</p> <p>? evaluation of the knowledge and skills related to the realization of laboratory exercise, the evaluation of the report from done exercises.</p> <p>Obtaining additional points for activity during exercises, in particular way for:</p> <p>? proposing to discuss additional aspects of the subject</p> <p>? effective use of knowledge obtained during solving of given problem;</p> <p>? comments related to improve teaching material,</p> <p>? aesthetics of solved problems and reports ? within homework.</p>		
<b>Course description</b>		
<p>The characteristics of the modulation methods. Goals and kinds of the modulation. The classification of the modulation systems. Analog modulations. Impulse modulations. Digital modulations. Use of chosen of the modulation methods in the power electronics. Kinds and properties of the MSI modulation. Voltage and current modulation in power electronics systems.</p>		
<p><b>Basic bibliography:</b></p> <ol style="list-style-type: none"> <li>1. Izydorczyk J.: Płonka G., Tyma G., Teoria sygnałów, Wstęp, Wyd. Helion, 1999.</li> <li>2. Mikołajuk K., Podstawy analizy obwodów energoelektronicznych, Państwowe Wydawnictwo Naukowe, Warszawa 1998.</li> <li>3. Szabatin J.: Podstawy teorii sygnałów, WKiŁ, Warszawa 1982.</li> <li>4. Tunia H., Smirnow A., Nowak M., Barlik R., Układy energoelektroniczne. Obliczanie, modelowanie, projektowanie, Wydawnictwa Naukowo-Techniczne, Warszawa 1982.</li> </ol>		
<p><b>Additional bibliography:</b></p> <ol style="list-style-type: none"> <li>1. Lyons R.G.: Wprowadzenie do cyfrowego przetwarzania sygnałów, WKiŁ, Warszawa 1999.</li> <li>2. Mohan N., Undeland N., Robins W., Power Electronics, Jon Wiley &amp; Sons Inc., New York 1999.</li> </ol>		
<b>Result of average student's workload</b>		
<b>Activity</b>	<b>Time (working hours)</b>	
1. participation in the lectures	15	
2. participation in the laboratory exercises	15	
3. participation in consultations on the lecture	5	
4. participation in consultations on the laboratory exercises	10	
5. preparation for the laboratory exercises	10	
6. preparation for the exam	10	
7. preparation for the laboratory exercises pass	10	
8. participation in the exam	5	
<b>Student's workload</b>		
<b>Source of workload</b>	<b>hours</b>	<b>ECTS</b>
Total workload	80	3
Contact hours	50	2
Practical activities	15	1